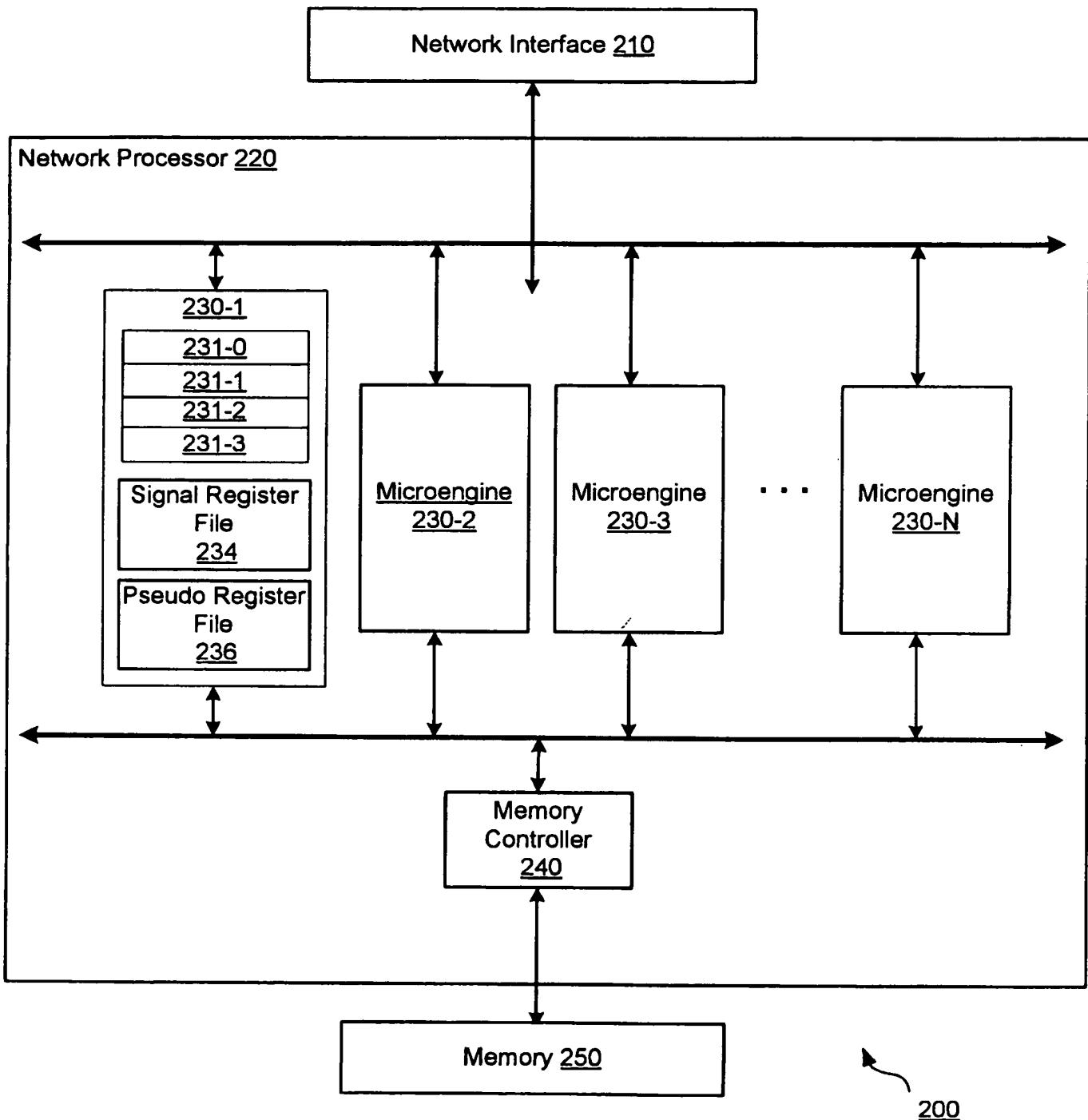


100

FIG. 1

**FIG. 2**

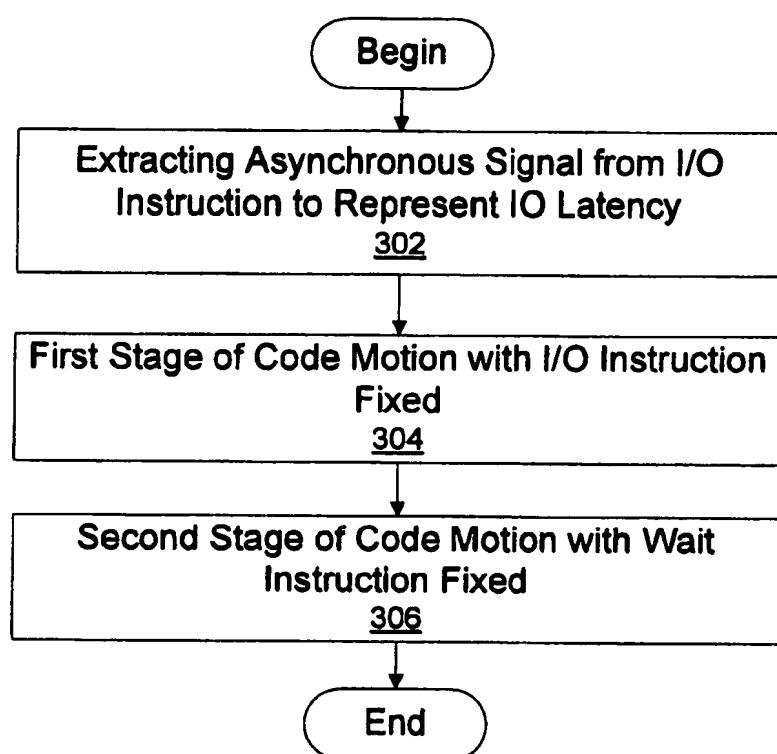


FIG. 3

[R2]=store R1 \downarrow 412
 R3=R2+1

... ...

R4=R3<<2
 R1=R5-R4

[R2]=store R1, signal s \downarrow 422
 Wait s \downarrow
 R3=R2+1

 R4=R3<<2
 R1=R5-R4

[R2]=store R1, signal s

Wait s

R1=R5-R4 \downarrow 436

\downarrow 400

FIG. 4A

FIG. 4B

FIG. 4C

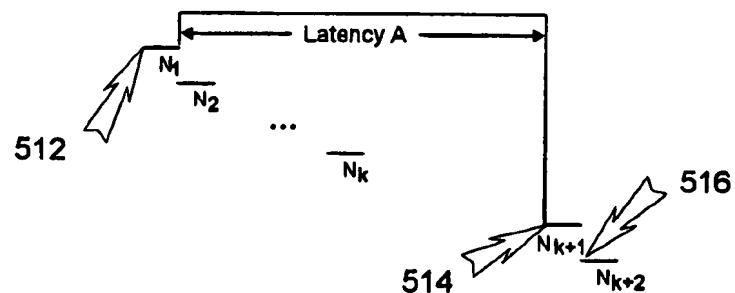


FIG. 5A

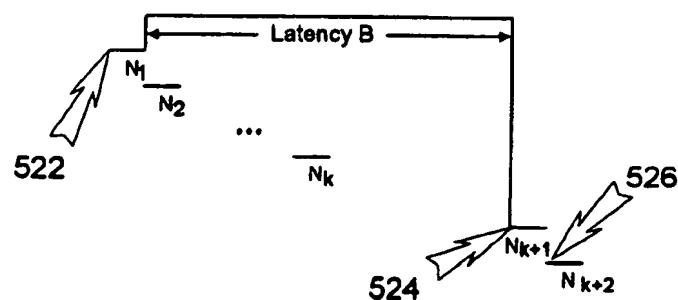


FIG. 5B

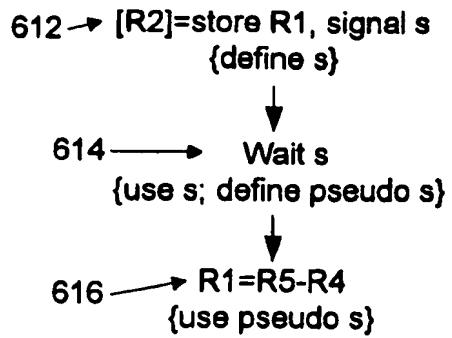


FIG. 6A

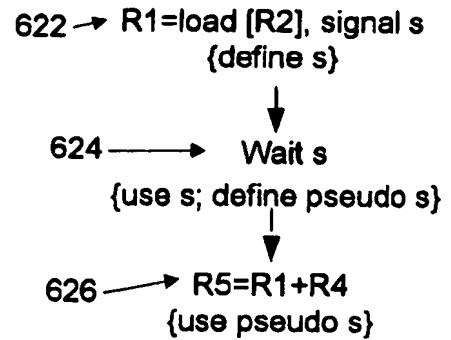


FIG. 6B

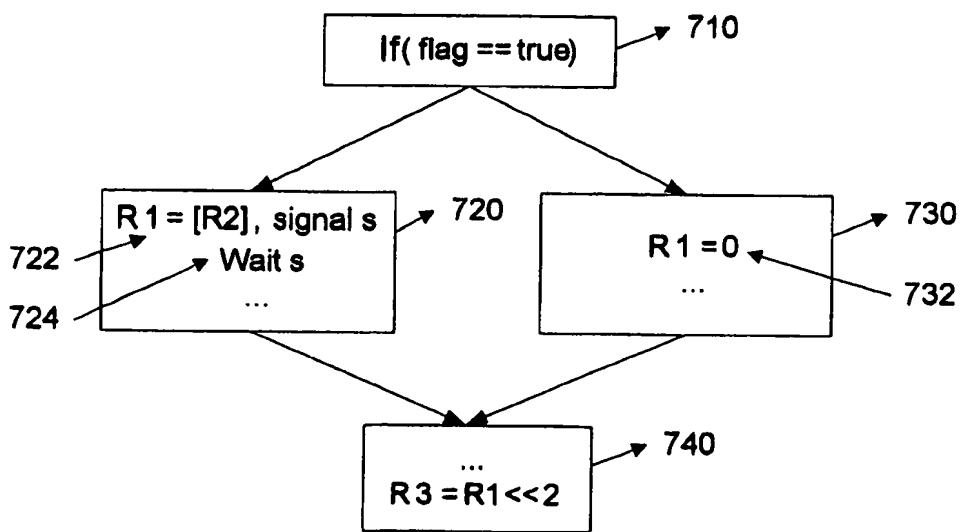


FIG. 7A

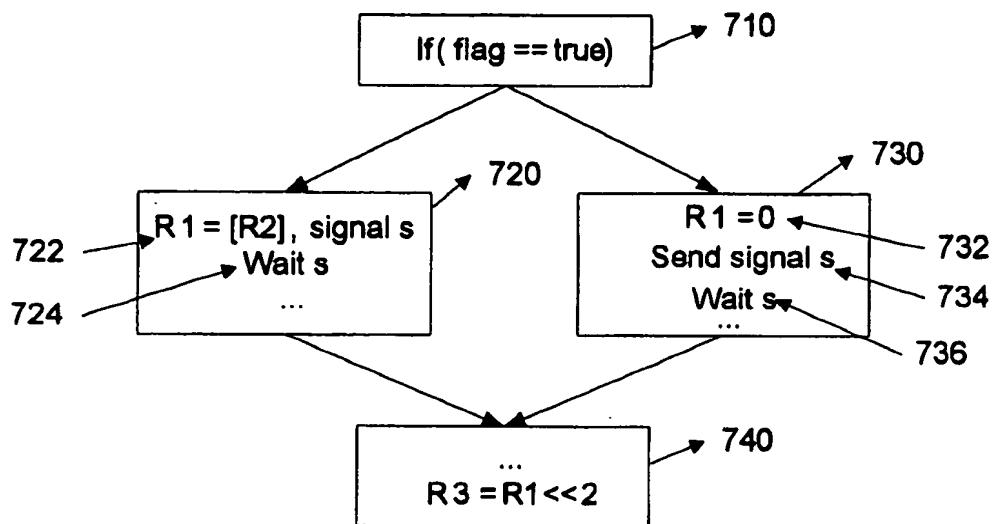


FIG. 7B

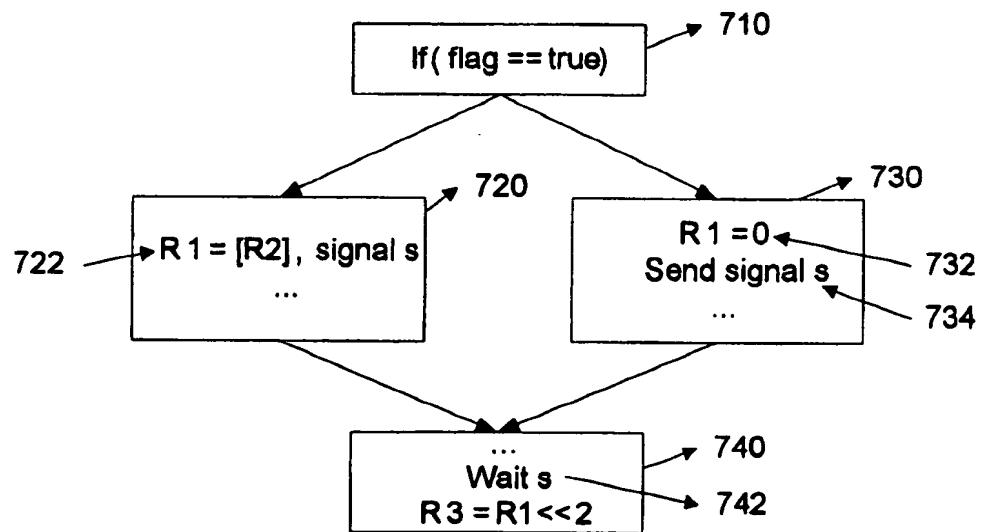


FIG. 7C

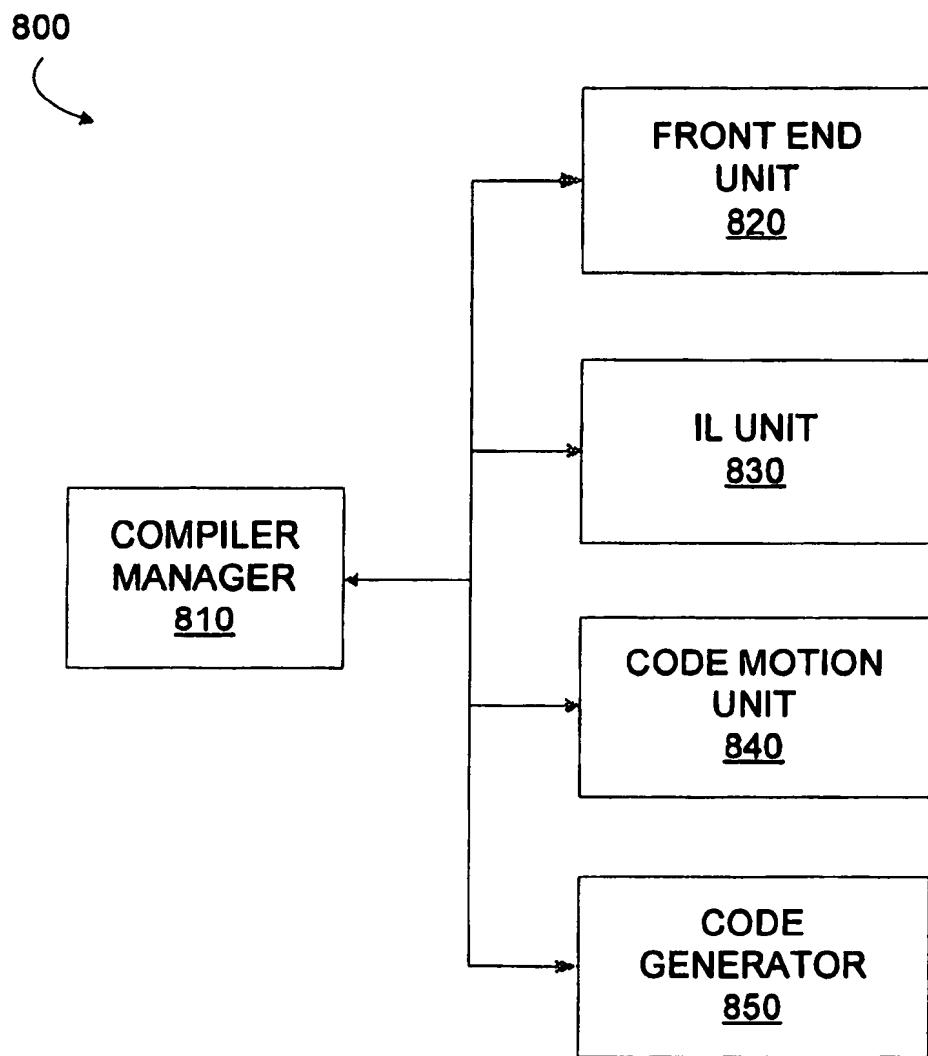


FIG. 8